

Best Available Copy

SEARCHED

Class	Sub.	Date	Exmr.
703	15	6-27-03	Dmc
updated		11-19-03	Dmc
703	16	11-19-03	Dmc
710	24	1	1
updated		6-6-04	Dmc

INTERFERENCE SEARCHED

Class	Sub.	Date	Exmr.
703	15	11-19-03	Dmc
1	16	1	1

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

EAST ←	Date	Exmr.
processor simulation	6-19-03	Dmc
memory	6-27-03	Dmc
system on a chip		
hardware description		
language		
memory processor		
cycle event		
model		
virtual		
prototypes		
cycle based		
cycle event		
cycle accurate		
event driven		
EAST simulation	6-24-03	Dmc
component		
sub-component		
blank prototype		
updated	11-19-03	Dmc
EAST	11-19-03	Dmc
processor		
simulation		
memory		
system		
IEEE	11-19-03	Dmc
system simulation		
cycle function		
communication		
2123 Bill Thomson RA	11-20-03	Dmc
2123 Kevin Teska SPE		
2124 John Chavis	11-20-03	Dmc
function call	11-20-03	Dmc
high level language		
programming		
EAST	6-4-04	Dmc
cycle accurate		
function calls		
clearth.		